Refine Search

Search Results -

Terms	Documents
L1 same (message or instruction or command)	33

Database:

```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search:

```
L3
```

[Refine Search]

[Recall Text ⬍]   [Clear]   [Interrupt]

Search History

DATE: **Wednesday, January 04, 2006**   Printable Copy   Create Case

Set Name Query	Hit Count	Set Name
side by side		result set
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
L3 L1 same (message or instruction or command)	33	L3
L2 L1 and (message or instruction or command)	66	L2
L1 processor same initializ$3 same controller same (disabl$3 or (single adj1 function))	73	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L3	0

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:
L4

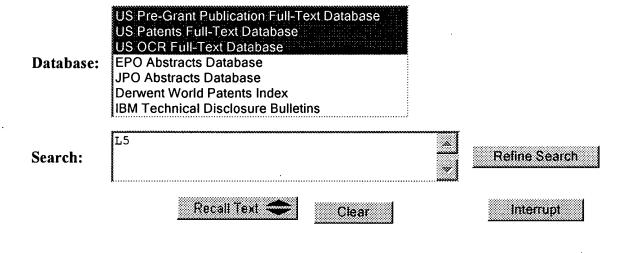[Refine Search]

[Recall Text] [Clear]   [Interrupt]

Search History

DATE: Wednesday, January 04, 2006   Printable Copy   Create Case

Set Name Query	Hit Count	Set Name
side by side		result set
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L4 L3	0	L4
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
L3 L1 same (message or instruction or command)	33	L3
L2 L1 and (message or instruction or command)	66	L2
L1 processor same initializ$3 same controller same (disabl$3 or (single adj1 function))	73	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(361/683 |361/686 |370/257 |370/453 |709/208 |710/305 |710/104 |710/313 |710/314 |710/315 |710/10 |710/110 |711/100 |711/147 |711/154 |712/32 |712/36 |719/321 |719/327 |713/1).ccls.	15509

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L5

Refine Search

Recall Text ⇕   Clear   Interrupt

Search History

DATE: Wednesday, January 04, 2006 Printable Copy   Create Case

Set Name Query side by side	Hit Count	Set Nam resul set
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
L5 710/305,104,313-315,10,110;711/100,147,154;361/683,686;713/1;709/208;719/321,327;712/32,36;370/257,453.ccls.	15509	L5
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L4 L3	0	L4
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
L3 L1 same (message or instruction or command)	33	L3
L2 L1 and (message or instruction or command)	66	L2
L1 processor same initializ$3 same controller same (disabl$3 or (single adj1 function))	73	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L3 and L5	5

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L6

[Refine Search]

[Recall Text] [Clear]   [Interrupt]

Search History

DATE: Wednesday, January 04, 2006   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Nam resul set
	DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
L6	l3 and L5	5	L6
L5	710/305,104,313-315,10,110;711/100,147,154;361/683,686;713/1;709/208;719/321,327;712/32,36;370/257,453.ccls.	15509	L5
	DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L4	L3	0	L4
	DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
L3	L1 same (message or instruction or command)	33	L3
L2	L1 and (message or instruction or command)	66	L2
L1	processor same initializ$3 same controller same (disabl$3 or (single adj1 function))	73	L1

END OF SEARCH HISTORY

IEEE Xplore® RELEASE 2.1

Welcome United States Patent and Trademark Office

:::Search Results

BROWSE   SEARCH   IEEE XPLORE GUIDE   SUPPORT

Results for "(processor and initializ* and controller<in>metadata) and disabl* and (message or instructio..."

Your search matched **34** of **1293212** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

☑ e-mail 🖨 printer friendly

» Search Options

View Session History

New Search

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

(processor and initializ* and controller<in>metadata) and disabl* and (message or in ≫

☐ Check to search only within this results set

Display Format: ⦿ Citation   ○ Citation & Abstract

Select   Article Information

1-25 | 26-34

☐ 1. **Overview of the Architecture, Circuit Design, and Physical Implementation of a First-Generation Cell Processor**
Pham, D.C.; Aipperspach, T.; Boerstler, D.; Bolliger, M.; Chaudhry, R.; Cox, D.; Harvey, P.; Harvey, P.M.; Hofstee, H.P.; Johns, C.; Kahle, J.; Kameyama, A.; Keaty, J.; Masubuchi, Y.; Pham, M.; Pille, J.; Posluszny, S.; Riley, M.; Stasiak, D.L.; Suzuoki, M.; Takahashi, O.; Warnock, J.; Weitzel, S.; Wendel, D.; Yazawa, K.;
Solid-State Circuits, IEEE Journal of
Volume 41, Issue 1, Jan. 2006 Page(s):179 - 196
Digital Object Identifier 10.1109/JSSC.2005.859896
AbstractPlus | Full Text: PDF(2880 KB)   IEEE JNL

☐ 2. **A universal test and maintenance controller for modules and boards**
Lien, J.-C.; Breuer, M.A.;
Industrial Electronics, IEEE Transactions on
Volume 36, Issue 2, May 1989 Page(s):231 - 240
Digital Object Identifier 10.1109/41.19074
AbstractPlus | Full Text: PDF(924 KB)   IEEE JNL

☐ 3. **A peripheral-oriented microcomputer system**
Bass, J.E.;
Proceedings of the IEEE
Volume 64, Issue 6, June 1976 Page(s):860 - 873
AbstractPlus | Full Text: PDF(1185 KB)   IEEE JNL

☐ 4. **An operating system for a fault-tolerant multiprocessor controller**
Williams, R.D.; Johnson, B.W.; Roberts, T.E.;
Micro, IEEE
Volume 8, Issue 4, Aug. 1988 Page(s):18 - 29
Digital Object Identifier 10.1109/40.7769
AbstractPlus | Full Text: PDF(1140 KB)   IEEE JNL

☐ 5. **A proposed application of an industrial DOS computer for distribution substation monitoring and control**
Batur, I.; Guven, A.N.; Ozay, N.;
Power Delivery, IEEE Transactions on
Volume 9, Issue 2, April 1994 Page(s):729 - 735
Digital Object Identifier 10.1109/61.296250
AbstractPlus | Full Text: PDF(664 KB)   IEEE JNL

☐ 6. **Scheduling for reactive real-time systems**
Pai Chou; Walkup, E.A.; Borriello, G.;
Micro, IEEE
Volume 14, Issue 4, Aug. 1994 Page(s):37 - 47
Digital Object Identifier 10.1109/40.296156

AbstractPlus | Full Text: PDF(2436 KB)   IEEE JNL

7. **32-bit processing unit for embedded space flight applications**
Stachetti, V.; Gaisler, J.; Goller, G.; Le Gargasson, C.;
Nuclear Science, IEEE Transactions on
Volume 43, Issue 3, Part 1, June 1996 Page(s):873 - 878
Digital Object Identifier 10.1109/23.510727

AbstractPlus | Full Text: PDF(460 KB)   IEEE JNL

8. **A multiprocessor control system architecture for a cascaded StatCom with energy storage**
Qian, C.; Crow, M.L.; Atcitty, S.;
Applied Power Electronics Conference and Exposition, 2004. APEC '04. Nineteenth Annual IEEE
Volume 3, 2004 Page(s):1757 - 1763 Vol.3
Digital Object Identifier 10.1109/APEC.2004.1296104

AbstractPlus | Full Text: PDF(1516 KB)   IEEE CNF

9. **Software-only bus encoding techniques for an embedded system**
Wei-Chung Cheng; Jian-Lin Liang; Pedram, M.;
Design Automation Conference, 2002. Proceedings of ASP-DAC 2002. 7th Asia and South Pacific and the 15th
International Conference on VLSI Design. Proceedings.
7-11 Jan. 2002 Page(s):126 - 131
Digital Object Identifier 10.1109/ASPDAC.2002.994898

AbstractPlus | Full Text: PDF(305 KB)   IEEE CNF

10. **IEEE standard for high-bandwidth memory interface based on Scalable Coherent Interface (SCI) signaling technology (RamLink)**
IEEE Std 1596.4-1996
15 Sept. 1996

AbstractPlus | Full Text: PDF(968 KB)   IEEE STD

11. **IEEE standard microcontroller system serial control bus**
IEEE Std 1118-1990
5 Aug. 1991

AbstractPlus | Full Text: PDF(8732 KB)   IEEE STD

12. **Hybrid supervisory control for real-time embedded bus rapid transit applications**
Girard, A.R.; Howell, A.S.; Hedrick, J.K.;
Vehicular Technology, IEEE Transactions on
Volume 54, Issue 5, Sept. 2005 Page(s):1684 - 1696
Digital Object Identifier 10.1109/TVT.2005.853466

AbstractPlus | Full Text: PDF(720 KB)   IEEE JNL

13. **Multi-microprocessors: An overview and working example**
Fuller, S.H.; Ousterhout, J.K.; Raskin, L.; Rubinfeld, P.I.; Sindhu, P.J.; Swan, R.J.;
Proceedings of the IEEE
Volume 66, Issue 2, Feb. 1978 Page(s):216 - 228

AbstractPlus | Full Text: PDF(2445 KB)   IEEE JNL

14. **A microcomputer-based speed controller for lift drives**
Petrovcic, J.; Strmcnik, S.;
Industry Applications, IEEE Transactions on
Volume 24, Issue 3, May-June 1988 Page(s):487 - 498
Digital Object Identifier 10.1109/28.2900

AbstractPlus | Full Text: PDF(1028 KB)   IEEE JNL

15. **A fault-tolerant multiprocess controller for magnetic bearings**
Yates, S.W.; Williams, R.D.;
Micro, IEEE
Volume 8, Issue 4, Aug. 1988 Page(s):6 - 17
Digital Object Identifier 10.1109/40.7768

AbstractPlus | Full Text: PDF(912 KB)   IEEE JNL

16. **A microprocessor-controlled door lock system**
Poirier, D.C.; Vishnubhotla, S.R.;
Consumer Electronics, IEEE Transactions on
Volume 36, Issue 2, May 1990 Page(s):129 - 136
Digital Object Identifier 10.1109/30.54279

AbstractPlus | Full Text: PDF(456 KB)   IEEE JNL

17. **Analysis and synthesis of concurrent digital circuits using control-flow expressions**
Coelho, C.N., Jr.; De Micheli, G.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
Volume 15, Issue 8, Aug. 1996 Page(s):854 - 876
Digital Object Identifier 10.1109/43.511567

AbstractPlus | References | Full Text: PDF(2248 KB)   IEEE JNL

18. **Application-specific programmable control for high-performance asynchronous circuits**
Jacobson, H.M.; Gopalakrishnan, G.;
Proceedings of the IEEE
Volume 87, Issue 2, Feb. 1999 Page(s):319 - 331
Digital Object Identifier 10.1109/5.740024

AbstractPlus | References | Full Text: PDF(360 KB)   IEEE JNL

19. **The system design of a Windows CE ARM based micro-controller**
Clarke, K.S.P.; Kershaw, D.;
Computer Design: VLSI in Computers and Processors, 1998. ICCD '98. Proceedings., International Conference on
5-7 Oct. 1998 Page(s):236 - 241
Digital Object Identifier 10.1109/ICCD.1998.727056

AbstractPlus | Full Text: PDF(44 KB)   IEEE CNF

20. **IEEE recommended practice for electric power systems in commercial buildings**
IEEE Std 241-1990
18 Sept. 1991

AbstractPlus | Full Text: PDF(7452 KB)   IEEE STD

21. **Implementation and use of small automated-test systems**
Mukaihata, T.; Johnstone, R.D.;
Proceedings of the IEEE
Volume 66, Issue 4, April 1978 Page(s):403 - 413

AbstractPlus | Full Text: PDF(1030 KB)   IEEE JNL

22. **Autonet: a high-speed, self-configuring local area network using point-to-point links**
Schroeder, M.D.; Birrell, A.D.; Burrows, M.; Murray, H.; Needham, R.M.; Rodeheffer, T.L.; Satterthwaite, E.H.; Thacker, C.P.;
Selected Areas in Communications, IEEE Journal on
Volume 9, Issue 8, Oct. 1991 Page(s):1318 - 1335
Digital Object Identifier 10.1109/49.105178

AbstractPlus | Full Text: PDF(1732 KB)   IEEE JNL

23. **The AT&T satellite network control system**
Brostrup-Jensen, P.; Moshenberg, D.;
Selected Areas in Communications, IEEE Journal on
Volume 10, Issue 6, Aug. 1992 Page(s):1094 - 1102
Digital Object Identifier 10.1109/49.144895

AbstractPlus | Full Text: PDF(852 KB)   IEEE JNL

24. **DSS: a distributed high-level synthesis system**
Roy, J.; Kumar, N.; Dutta, R.; Vemuri, R.;
Design & Test of Computers, IEEE
Volume 9, Issue 2, June 1992 Page(s):18 - 32
Digital Object Identifier 10.1109/54.143143

AbstractPlus | Full Text: PDF(1132 KB)   IEEE JNL

25. Failure mode testing tool set for automotive electronic controllers
Wagner, J.R.;
Vehicular Technology, IEEE Transactions on
Volume 43, Issue 1, Feb. 1994 Page(s):156 - 163
Digital Object Identifier 10.1109/25.282276

AbstractPlus | Full Text: PDF(776 KB)   IEEE JNL

IEEE Xplore RELEASE 2.1

Welcome United States Patent and Trademark Office

Search Results

BROWSE   SEARCH   IEEE XPLORE GUIDE   SUPPORT

Results for "(processor and initializ* and controller<in>metadata) and disabl* and (message or instructio..."

Your search matched **34** of **1293212** documents.

A maximum of **34** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

☑ e-mail   🖨 printer friendly

» Search Options

<u>View Session History</u>

<u>New Search</u>

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

(processor and initializ* and controller<in>metadata) and disabl* and (message or in   »

☐ Check to search only within this results set

Display Format: ⦿ Citation   ◯ Citation & Abstract

Select   Article Information   1-25 | 26-34

☐ 26. **Implementation of a MAC-layer protocol (GIT-CSMA/CD) for industrial LAN's and its experimental performance**
Kapsalis, V.D.; Koubias, S.A.; Papadopoulps, G.D.;
Industrial Electronics, IEEE Transactions on
Volume 44, Issue 6, Dec. 1997 Page(s):825 - 839
Digital Object Identifier 10.1109/41.649944

AbstractPlus | References | Full Text: PDF(408 KB)   IEEE JNL

☐ 27. **Sensorless vector controller for a synchronous reluctance motor**
Jovanovic, M.G.; Betz, R.E.; Platt, D.;
Industry Applications, IEEE Transactions on
Volume 34, Issue 2, March-April 1998 Page(s):346 - 354
Digital Object Identifier 10.1109/28.663478

AbstractPlus | References | Full Text: PDF(228 KB)   IEEE JNL

☐ 28. **An efficient quality-aware memory controller for multimedia platform SoC**
Kun-Bin Lee; Tzu-Chieh Lin; Chein-Wei Jen;
Circuits and Systems for Video Technology, IEEE Transactions on
Volume 15, Issue 5, May 2005 Page(s):620 - 633
Digital Object Identifier 10.1109/TCSVT.2005.846412

AbstractPlus | References | Full Text: PDF(1704 KB)   IEEE JNL

☐ 29. **An onboard processor and adaptive scanning controller for the Second-Generation Precipitation Radar**
Fischman, M.A.; Berkun, A.C.; Chun, W.; Im, E.; Andraka, R.J.;
Geoscience and Remote Sensing, IEEE Transactions on
Volume 43, Issue 4, April 2005 Page(s):802 - 812
Digital Object Identifier 10.1109/TGRS.2005.844670

AbstractPlus | References | Full Text: PDF(1848 KB)   IEEE JNL

☐ 30. **AVATAR: Second-generation transceiver electronics for ocean acoustic tomography**
Worcester, P.; Peckham, D.; Hardy, K.; Dormer, F.;
OCEANS
Volume 17, Nov 1985 Page(s):654 - 662

AbstractPlus | Full Text: PDF(736 KB)   IEEE CNF

☐ 31. **Message-oriented decomposition for modeling supervisory control in manufacturing system**
Du, X.H.; Chen Zhou;
Robotics and Automation, 1999. Proceedings. 1999 IEEE International Conference on
Volume 2, 10-15 May 1999 Page(s):1456 - 1461 vol.2
Digital Object Identifier 10.1109/ROBOT.1999.772565

AbstractPlus | Full Text: PDF(464 KB)   IEEE CNF

32. **Instruction-driven wake-up mechanisms for Snoopy TAP controller**
Bhattacharya, D.;
VLSI Test Symposium, 1999. Proceedings. 17th IEEE
25-29 April 1999 Page(s):467 - 472
Digital Object Identifier 10.1109/VTEST.1999.766705

AbstractPlus | Full Text: PDF(268 KB)   IEEE CNF

33. **IEEE standard multiple controllers in a CAMAC crate**
ANSI/IEEE Std 675-1982
26 Feb. 1982

AbstractPlus | Full Text: PDF(860 KB)   IEEE STD

34. **IEEE standard backplane bus specification for multiprocessor architectures: Futurebus**
ANSI/IEEE Std 896.1-1987
28 July 1988

AbstractPlus | Full Text: PDF(5132 KB)   IEEE STD

1-25 | 26-34

Help   Contact Us   Privacy & Security   IEEE.org

Indexed by
Inspec

IEEE Xplore
RELEASE 2.1

Welcome United States Patent and Trademark Office

Search Results   BROWSE   SEARCH   IEEE XPLORE GUIDE   SUPPORT

Results for "(processor and initializ* and controller<in>metadata) and disabl* and (message or instructio..."

📧 e-mail 🖨 printer friendly

Your search matched **0** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» Search Options

View Session History

New Search

Modify Search

(processor and initializ* and controller<in>metadata) and disabl* and (message or in >>

☐ Check to search only within this results set

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Display Format: ● Citation   ○ Citation & Abstract

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revising your search.

Indexed by
Inspec

EAST - [Untitled1:1]

File View Edit Tools Window Help

- Drafts
- Pending
- Active
  - L1: (51) processor same
  - L2: (45) l1 and (messag
  - L3: (21) l1 same (messa
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search | Edit | Browse | Queue | Clear

DBs | USPAT

☑ Plurals
☑ Highlight all hit terms initially

Default operator | OR ▾

BRS form | IS&R form | Image | Text | HTML

	Type	L #	Hits	Search Text	DBs	Time Stam	Comment	Error Definit	Er
1	BRS	L1	51	processor same initializ$3 same cont	USPA T	2006/01/0 4 16:23			
2	BRS	L2	45	l1 and (message or instruction or comman	USPA T	2006/01/0 4 16:24			
3	BRS	L3	21	l1 same (message or instruction or comman	USPA T	2006/01/0 4 16:24			

Start | EAST - [...

EAST - [Untitled1:1]

File   View   Edit   Tools   Window   Help

- Drafts
- Pending
- Active
  - L1: (51) processor same
  - L2: (45) l1 and (messag
  - L3: (21) l1 same (mess:
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search   List   **Browse**   Queue   **Clear**

DBs   USPAT

☑ Plurals
☑ Highlight all hit terms initially

Default operator: OR   ▼

l1 same (message or instruction or command)

BRS form   IS&R form   Image   Text   HTML

	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current X
1	☐	☐	US 6931865 B1	20050823	12	Method and apparatus for determining coolant	62/129	123/41.01 701/34;
2	☐	☐	US 6824528 B1	20041130	36	Method and apparatus for pressure infusion a	604/113	604/142; 604/253
3	☐	☐	US 6819599 B2	20041116	16	Programmable DQS preamble	365/189.07	365/189.0 ;
4	☐	☐	US 6810373 B1	20041026	10	Method and apparatus for modeling using a ha	703/14	703/25; 716/5
5	☐	☐	US 6678804 B1	20040113	20	Apparatus and method for memory access contr	711/154	711/104
6	☐	☐	US 6661671 B1	20031209	16	Apparatus, method and article of manufacture	361/752	361/686; 361/796;
7	☐	☐	US 6651171 B1	20031118	16	Secure execution of program code	713/193	711/163; 711/164;
8	☐	☐	US 6609034 B1	20030819	27	System and method for remotely controlling an	700/19	361/681; 361/682;
9	☐	☐	US 6185633 B1	20010206	51	DMA configurable receive channel with me	710/22	
10	☐	☐	US 6085332 A	20000704	11	Reset design for redundant raid controll	714/5	710/1; 711/114
11	☐	☐	US 6065070	20000516	34	DMA configurable	710/22	709/212;

Start   EAST - [...